

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of: Junji ICHIMIYA  
Serial No. : Not Yet Known  
Date Filed : Herewith  
For : LAYOUT DESIGN METHOD FOR SEMICONDUCTOR  
INTEGRATED CIRCUIT, AND SEMICONDUCTOR INTEGRATED  
CIRCUIT

**Mail Stop Patent Application**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INFORMATION DISCLOSURE STATEMENT**

Pursuant to the applicant's duty of disclosure, the information listed in the attached Form PTO-1449 is brought to the attention of the Examiner. A copy of the information identified herein is also provided.

It is respectfully requested that the information cited in annexed Form PTO-1449 be considered by the Examiner in connection with the above-identified patent application, filed concurrently herewith, and that such art be made of record in said application.

The citation of the listed items is not a representation that they constitute a complete or exhaustive listing of the relevant art or that these items are prior art. The items listed are submitted in good faith, but are not intended to substitute for the Examiner's search. It is hoped, however, that in addition to apprising the Examiner of the particular items, they will assist in identifying fields of search and in making as full and complete a search as possible.

The filing of this Information Disclosure Statement is not an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

The following commonly-owned U.S. patent application is brought to the attention of the Examiner:

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Dkt. 2271/71388

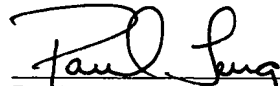
(a) U.S. Patent Appl. No. 09/951,071 of ICHIMIYA et al., filed September 13, 2001.

A copy of the commonly-owned U.S. patent application is enclosed herewith.

The Office is hereby authorized to charge any additional fees which may be required for consideration of this Information Disclosure Statement and to credit any overpayment to our Deposit Account No. 03-3125.

Early and favorable consideration of the case is respectfully requested.

Respectfully submitted,



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Form PTO-1449		U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. 2271/71388		Serial No. Not Yet Known							
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				Applicant: Junji ICHIMIYA									
				Filing Date: Herewith		Group Art Unit:							
U.S. PATENT DOCUMENTS													
Examiner Initial		Document Number						Date	Name	Class	Subclass	Filing Date if Appropriate	
FOREIGN PATENT DOCUMENTS													
		Document Number						Date	Country	Class	Subclass	Translation Yes      No	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)													
		U.S. Patent Application No. 09/951,071 to ICHIMIYA et al., filed September 13, 2001.											
EXAMINER				DATE CONSIDERED									
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.													